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High Speed Digital Design A

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Let's now consider what happens when a high speed edge is generated at the output of a digital device,

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and switches from a logic 0 (0V) to 1 (3.3V). For the purposes of this example, the edge has a rise time of 200ps and switches at a rate of 10 MHz. The first thing to note is that the 10 MHz switching rate is irrelevant.

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digital electronic design is certainly no exception. If your product must work with large volumes of data (such as video, networking, or any component or peripheral of a personal computer or smartphone), competent high-speed digital design will be a critical component to its success.

High-Speed Digital Design (Part 1) | SIGMADESIGN

High-speed Digital Design: A Handbook of Black Magic. High-speed Digital Design. : Howard W. Johnson, Martin Graham. Prentice Hall, 1993 - Technology & Engineering - 447 pages. 1 Review. Focused on the field of knowledge lying between digital and analog circuit theory, this new text will help engineers working with digital systems shorten their product development cycles and help fix their latest design problems.

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